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How to evaluate Deep
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Approaches

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Beginners Part 1 - Learn
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- Tensorflow

optimizations and

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~~Deep Neural Networks~~

How Deep Neural

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~~Understanding Reuse,~~

~~Performance, and~~

~~Hardware Cost of DNN~~

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PyTorch in 5 Minutes
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1950 to 2018

Model Quantization in
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(Post Training) ~~PyTorch~~
~~Hooks Explained~~ ~~In-~~
~~depth Tutorial~~ ~~What are~~
~~carbon emissions?~~

Quantization Error in
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Conference 2018:~~

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~~PyTorch Quantization
Chris Gottbrath~~

~~Distributed Authority~~

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~~Framework Fp Dnn An~~

~~Automated Framework~~

To overcome this
problem, we propose FP-

DNN (Field Deep

Programmable DNN),

an end-to-end

framework that takes

TensorFlow-described

DNNs as input, and

automatically gen-erates

the hardware

implementations on

FPGA boards with RTL-

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HLS hybrid templates.

FP-DNN performs
model inference of
DNNs with our high-
performance
computation engine and

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FP-DNN: An
Automated Framework
for Mapping Deep

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Neural Networks onto
FPGAs with RTL-HLS
Hybrid Templates.

Abstract: DNNs (Deep
Neural Networks) have
demonstrated great
success in numerous
applications such as
image classification,
speech recognition,
video analysis, etc.

However, DNNs are
much more computation-
intensive and memory-

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intensive than previous
shallow models.

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FP-DNN: An
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for Mapping Deep
Neural Networks onto
FPGAs with RTL-HLS
Hybrid Templates.

DNNs (Deep Neural

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Networks) have demonstrated great success in numerous applications such as image classification, speech recognition, video analysis, etc. [...]

To overcome this problem, we propose FP-DNN (Field Programmable DNN), an end-to-end framework that takes TensorFlow-described

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DNNs as input, and
automatically generates
the hardware
implementations on
FPGA boards with RTL-
HLS ...

~~[PDF] FP-DNN: An
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for Mapping Deep ...~~

Neural fp dnn an
automated framework
part. FP-DNN
automatically generates

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the hardware im-
plementation with RTL-
HLS hybrid templates.
Our framework can
support almost all types
of DNNs, and we
implement several
DNNs (CNNs, LSTM-
RNNs, and Residual
Nets) as case studies.
FPGA-based acceler-
ators Download Fp Dnn
An Automated
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The authors in propose to use on-chip double buffers and develop a smart memory organization to overlap data transfers with computations. Later they propose an automated framework

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named FP-DNN to...

~~Automated
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Neural ...~~

Title: Fp Dnn An

Automated Framework
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Neural Author:

wiki.ctsnet.org-Marie Fa
erber-2020-09-21-10-51
-54 Subject: Fp Dnn An
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automatically generates
the hardware im-

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plementation with RTL-HLS hybrid templates. Our framework can support almost all types of DNNs, and we implement several DNNs (CNNs, LSTM-RNNs, and Residual Nets) as case studies. FPGA-based accelerators

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~~For Mapping~~

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automatically generates
the hardware im-
plementation with RTL-
HLS hybrid templates.

Our framework can
support almost all types
of DNNs, and we

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implement several
DNNs (CNNs, LSTM-
RNNs, and Residual
Nets) as case studies.

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Neural. prepare the fp
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deep neural to gate
every morning is usual
for many people.
However, there are yet
many people who as
well as don't once
reading. This is a
problem.

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Yijin Guan et al. FP-
DNN: An automated
framework for mapping
deep neural networks
onto FPGAs with RTL-
HLS hybrid templates.

In Proc. of FCCM,
2017. Google Scholar;
Yufei Ma et al. An
automatic RTL compiler
for high-throughput
FPGA implementation
of diverse deep

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overcome this problem,
we propose Page 4/25.
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Framework For
Mapping Deep
NeuralFP-DNN (Field
Programmable DNN),
an end-to-end
framework that takes
TensorFlow-described
DNNs as input, and
automatically gen-erates
the

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allowing you to get the
most less latency time to

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precision choice upon
the accuracy of a DNN.

Finally, we present a
semi-automatic software
framework for an
automatic precision and
accuracy analysis. Our

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versatile tool has a front-end accepting DNN models from common design frameworks such as Tensorflow/Keras, and is based on a generic error analysis technique, parametrizable by the target

~~A Framework for Semi-Automatic Precision and Accuracy ...~~

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Abstract—Deep Neural Networks (DNN) represent a performance-hungry application.

Floating-Point (FP) and custom floating-point-like arithmetic satisfies this hunger. While there is need for speed, inference in DNNs does not seem to have any need for precision.

Many papers experimentally observe

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that DNNs can
successfully run at
almost ridiculously

~~A Framework for Semi-
Automatic Precision and
Accuracy ...~~

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Author: mail.aiaraldea.e
us-2020-10-24T00:00:0
0+00:01 Subject: Fp
Dnn An Automated

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Framework For

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Keywords: fp, dnn, an,
automated, framework,

for, mapping, deep,

neural Created Date:

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This book constitutes
the proceedings of the

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16th International
Symposium on Applied
Reconfigurable
Computing, ARC 2020,
held in Toledo, Spain, in
April 2020. The 18 full
papers and 11 poster
presentations presented
in this volume were
carefully reviewed and
selected from 40
submissions. The papers
are organized in the
following topical

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sections: design
methods & tools; design
space exploration &
estimation techniques;
high-level synthesis;
architectures;
applications.

This book includes high-
quality research papers
presented at the 1st
International
Conference on Wireless
Sensor Networks,

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Ubiquitous Computing
and Applications
(ICWSNUCA, 2021),
which is held at
Gokaraju Rangaraju
Institute of Engineering
and Technology,
Hyderabad, India,
during 26–27 February,
2021. This volume
focuses on the
applications, use-cases,
architectures,
deployments, and recent

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Advances of wireless sensor networks as well as ubiquitous computing. Different research topics are illustrated in this book, like wireless sensor networks for the Internet of Things; IoT applications for eHealth; smart cities; architectures for WSNs and IoT, WSNs hardware and new devices; low-power

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wireless technologies;
wireless ad hoc sensor
networks; routing and
data transfer in WSNs;
multicast
communication in
WSNs; security
management in WSNs
and in IoT systems; and
power consumption
optimization in WSNs.

Deep learning
algorithms have brought

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a revolution to the computer vision community by introducing non-traditional and efficient solutions to several image-related problems that had long remained unsolved or partially addressed. This book presents a collection of eleven chapters where each individual chapter explains the deep

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learning principles of a specific topic, introduces reviews of up-to-date techniques, and presents research findings to the computer vision community. The book covers a broad scope of topics in deep learning concepts and applications such as accelerating the convolutional neural network inference on

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field-programmable gate
arrays, fire detection in
surveillance
applications, face
recognition, action and
activity recognition,
semantic segmentation
for autonomous driving,
aerial imagery
registration, robot
vision, tumor detection,
and skin lesion
segmentation as well as
skin melanoma

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classification. The content of this book has been organized such that each chapter can be read independently from the others. The book is a valuable companion for researchers, for postgraduate and possibly senior undergraduate students who are taking an advanced course in related topics, and for

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those who are interested in deep learning with applications in computer vision, image processing, and pattern recognition.

This book summarizes the key scientific outcomes of the Horizon 2020 research project TULIPP: Towards Ubiquitous Low-power Image Processing

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Platforms. The main focus lies on the development of high-performance, energy-efficient embedded systems for the growing range of increasingly complex image processing applications. The holistic TULIPP approach is described in the book, which addresses hardware platforms, programming

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tools and embedded
operating systems.

Several of the results are
available as open-source
hardware/software for
the community. The
results are evaluated
with several use cases
taken from real-world
applications in key
domains such as
Unmanned Aerial
Vehicles (UAVs),
robotics, space and

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medicine. Discusses the development of high-performance, energy-efficient embedded systems for the growing range of increasingly complex image processing applications; Covers the hardware architecture of embedded image processing systems, novel methods, tools and libraries for

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programming those systems as well as embedded operating systems to manage those systems; Demonstrates results with several challenging applications, such as medical systems, robotics, drones and automotive.

This Synthesis Lecture focuses on techniques

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for efficient data orchestration within DNN accelerators. The End of Moore's Law, coupled with the increasing growth in deep learning and other AI applications has led to the emergence of custom Deep Neural Network (DNN) accelerators for energy-efficient inference on edge devices. Modern

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DNNs have millions of hyper parameters and involve billions of computations; this necessitates extensive data movement from memory to on-chip processing engines. It is well known that the cost of data movement today surpasses the cost of the actual computation; therefore, DNN accelerators require

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careful orchestration of data across on-chip compute, network, and memory elements to minimize the number of accesses to external DRAM. The book covers DNN dataflows, data reuse, buffer hierarchies, networks-on-chip, and automated design-space exploration. It concludes with data orchestration

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challenges with compressed and sparse DNNs and future trends. The target audience is students, engineers, and researchers interested in designing high-performance and low-energy accelerators for DNN inference.

This two-volume set
LNCS 11314 and 11315
constitutes the

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thoroughly refereed
conference proceedings
of the 19th International
Conference on
Intelligent Data
Engineering and
Automated Learning,
IDEAL 2018, held in
Madrid, Spain, in
November 2018. The
125 full papers
presented were carefully
reviewed and selected
from 204 submissions.

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These papers provided a timely sample of the latest advances in data engineering and automated learning, from methodologies, frameworks and techniques to applications. In addition to various topics such as evolutionary algorithms, deep learning neural networks, probabilistic modelling, particle

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swarm intelligence, big data analytics, and applications in image recognition, regression, classification, clustering, medical and biological modelling and prediction, text processing and social media analysis.

This book constitutes the refereed proceedings of the 8th International

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Conference on Theory and Practice of Natural Computing, TPNC 2019, held in Kingston, ON, Canada, in December 2019. The 15 full papers presented in this book, together with two invited talk, were carefully reviewed and selected from 38 submissions. The papers are organized in topical sections named:

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Applications of Natural
Computing;
Evolutionary
Computation; Genetic
Algorithms, Swarm
Intelligence, and
Heuristics; Quantum
Computing and
Information.

The International
Conference on Field

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Programmable Logic
and Applications (FPL)
is the first and largest
conference covering the
rapidly growing area of
field programmable
logic During the past 26
years, many of the
advances achieved in
reconfigurable system
architectures,
applications, embedded
processors, design
automation methods

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(EDA) and tools have been first published in the proceedings of the FPL conference series. FPL 2016 will offer the following five conference tracks:

- Architectures and Technology,
- Applications and Benchmarks, Design Methods and Tools, Self-aware and Adaptive Systems, Surveys,

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